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CSE - A.

21PD03 - operating systems

CIT - II

① External fragmentation:

memory allocation in embedded systems.

Memory Poding.

Performance Optimization

② Round Robin scheduling: A time shared operating system is designed to support multiple user simultaneously by sharing the CPU time among them.

③ Sro	user level thread	Kernel level thread
1)	user thread are implemented by user.	kernel thread are implemented by operating system.
2)	The operating system doesn't recognize user threads.	The kernel threads are recognize by operating system.
3)	context switch time is less.	context switch time is more.
4)	no hardware support.	Hardware support is needed.

4) Semaphore:

Semaphore is a Synchronization mechanism that is used to coordinate Access to shared resources in a Concurrent program. A semaphore is a variable that is represented as an integer value and has two primary operation wait and signal.

5) Shortest Job first Scheduling because it is a scheduling algorithm that priorities tasks based on their runtime. The task with shortest runtime given highest priority and is executed first, which minimizes the average waiting time and maximizes the throughput.

S.No	Paging	Segmentation
1)	In paging, the program is divided into fixed (or) named size pages.	In Segmentation, the Program is divided into variable size section.
2)	For paging Operating System is accountable.	For Segmentation compiler is accountable.
3)	It is faster in comparison to Segmentation.	It is low.

7) Purpose of paging the page tables:

In paging process, page table stores definition of each pages. when an active process requests data Paging reduces external fragmentation.

Q8) A condition variable is a Synchronization mechanism that allows threads to block until a certain condition is satisfied. The operations on a condition variable are

wait
Signal
Broadcast.

Condition variable are an important tool for thread synchronization and can help prevent race condition, deadlocks and other synchronization issues in multi-thread programs.

Q9) Address binding refers to mapping of computer instruction and data to physical memory location.

Compile time Address binding.

Load time Address binding

Execution time Address binding.

Q10) Dispatch latency is amount of time it takes for Operating system to switch from one process to another. It is time between when operating system decides to switch from the currently running process to a different process when new process actually starts executing on CPU.

P.B

11. i) FCFS (first come first serve)

Gantt chart.

P_1	P_2	P_3	P_4	P_5	
0	10	11	13	14	19

Process	waiting time	Turnaround time	Completion time
P ₁	0	10	10
P ₂	10	11	11
P ₃	11	13	13
P ₄	13	14	14
P ₅	14	19	19
	48	67	67

waiting time = Completion time - Burst time (total waiting time) $\Rightarrow 48$ ms.

turnaround time = waiting time + Burst time (total Turnaround time) $\Rightarrow 67$ ms

Average waiting time = $48/5 = 9.6$ ms

ii) Shortest job first scheduling (SJF):

Gantt chart.

P_2	P_5	P_1	P_3	P_4	
0	1	6	16	18	9

Process	waiting time	Turnaround time	Completion time
P ₁	6	9	9
P ₂	0	1	1
P ₃	16	4	17
P ₄	18	2	20
P ₅	4	9	13
	48	35	35

Total turnaround time $\Rightarrow 36ms$

Total waiting time $\Rightarrow 16ms$

Average waiting time $= 16/5 = 3.2ms$

Priority Scheduling algorithm:

Gantt chart.

P_2	P_8	P_1	P_3	P_5	
0	1	6	16	18	19

Process	waiting time	Turnaround time	Completion time
P ₁	6	16	16
P ₂	0	1	1
P ₃	16	18	18
P ₄	18	19	19
P ₅	1	6	6
	41	60	60

Turnaround time $= 60ms$

Total waiting time $= 41ms$

Average waiting time $= 41/5 = 8.2ms$

iii) Shortest Job first Scheduling algorithm results is minimum average waiting time $= 3.2ms$. It means that process Process before a long one decreases the waiting time at the Short process more than it increases the waiting time of the long process.

13. i) P_2 will finish LAST. because A single processor system has three resource types x, y, z which are shared by the three processes. there are 5 units of each resource type.

Proc	x	y	z
P_0	1	2	1
P_1	2	0	1
P_2	2	2	1
Total	5	4	3

Request	x	y	z
P_0	1	0	3
P_1	0	1	2
P_2	1	2	0

P_1 will finish its execution first. once P_1 is done it release 2, 0, and 1 units of x, y, z . Now among P_0 and P_2 needs of process only be satisfied. So, P_0 finishes its execution. once P_2 is done it release 2, 2 and 1 units of x, y, z ... Now finally P_2 finishes its execution so P_2 is the one which finishes the end. P_2 process will finish LAST.

13. ii) 3 page frames (assume) 4, 7, 6, 1, 7, 6, 1, 2, 7, 2

First in First Out (FIFO)

4	7	6	1	7	6	1	2	7	2
4	4	4	1				1	1	
	7	7	7				7	7	
		6	6				6	7	

Total number of Page faults are = 6.

$$\text{Hit ratio} = \frac{\text{number of hits}}{\text{total number of page references}} \\ = \frac{4}{10} = 0.4$$

$$\text{miss ratio} = \frac{\text{number of page faults}}{\text{total number of page references}} \\ = \frac{6}{10} \\ = 0.6$$